

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant: §
Alessia Pavan et al. § Art Unit: 2894
§
Serial No.: 10/749,130 §
§ Examiner: Amar Movva
Filed: December 30, 2003 §
§
For: Non-Volatile Memory Cell § Conf. No.: 3296
Comprising Dielectric Layers §
Having a Low Dielectric §
Constant and Corresponding § Atty Docket: NUM.0104US
Manufacturing Process §

Mail Stop **Appeal Brief-Patents**
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

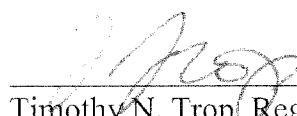
TRANSMITTAL OF AMENDED CLAIMS APPENDIX

In response to the Notification of Non-Compliant Appeal Brief, attached hereto is an Amended Claims Appendix. Please note that the rejection of only one claim is appealed.

The Commissioner is authorized to charge any fee due to Deposit Account No. 20-1504 (NUM.0104US).

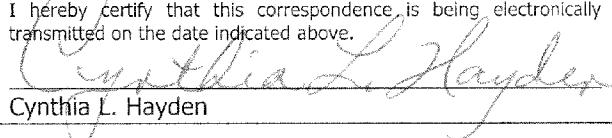
Respectfully submitted,

Date: August 11, 2009



Timothy N. Trop, Reg. No. 28,994
TROP, PRUNER & HU, P.C.
1616 South Voss Road, Suite 750
Houston, TX 77057-2631
713/468-8880 [Phone]
713/468-8883 [Fax]

Date of Deposit: August 11, 2009
I hereby certify that this correspondence is being electronically
transmitted on the date indicated above.



Cynthia L. Hayden

CLAIMS APPENDIX

The claims on appeal are:

39. A memory comprising:
a pair of adjacent cells having separate floating gates;
a field oxide between said cells;
a first dielectric covering said floating gates and said field oxide;
a second dielectric over said first dielectric between said floating gates, said second dielectric having a lower dielectric constant than said first dielectric; and
a control gate over said first and second dielectrics.
41. The memory of claim 39 wherein said first dielectric includes a nitride.
42. The memory of claim 41 wherein said first dielectric includes ONO.